

FII_PRA006-010_V1.0_R0.3

GPIO(PMOD X 2)

7-SEG DISPLAY x 6

LED X 8

USB 2 UART

AD/DA

**ALTERA
CYCLONE 10 LP
10CL006YE144C8G**

OSC_50M

BUTTON x 4

VGA

SWITCH x 8

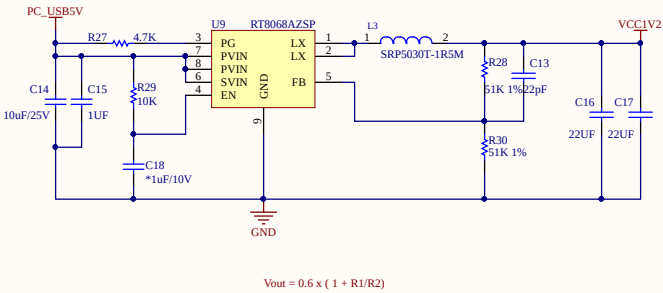
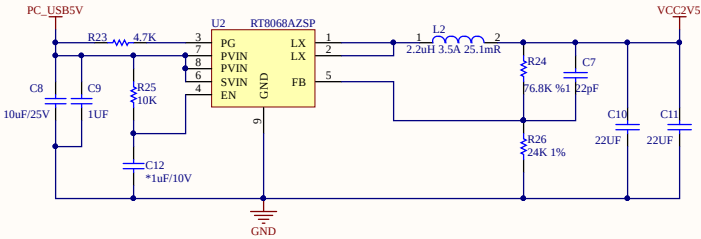
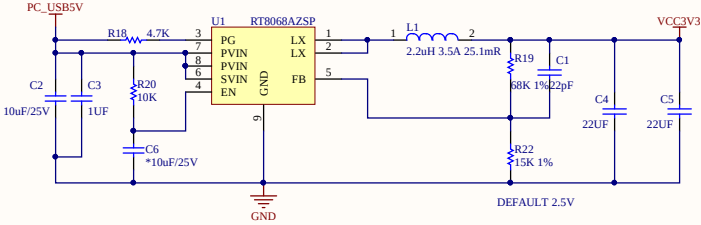
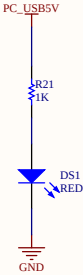
I2C EEPROM

ETHERNET(1G)

IIC Addressing	
8'B1010000X	IIC EEPROM
8'B1001000X	PCF8591T

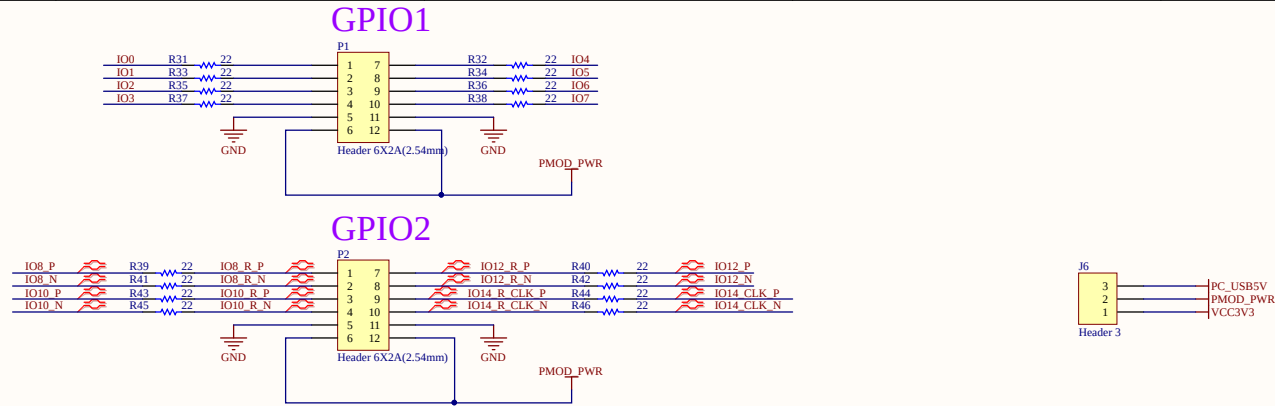
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Date: 2019-07-20	Time 3:41:33 PM	Engineer: WG RJ	Size: A4
Revision: R3.0		Sheet 1 of 14	

Power

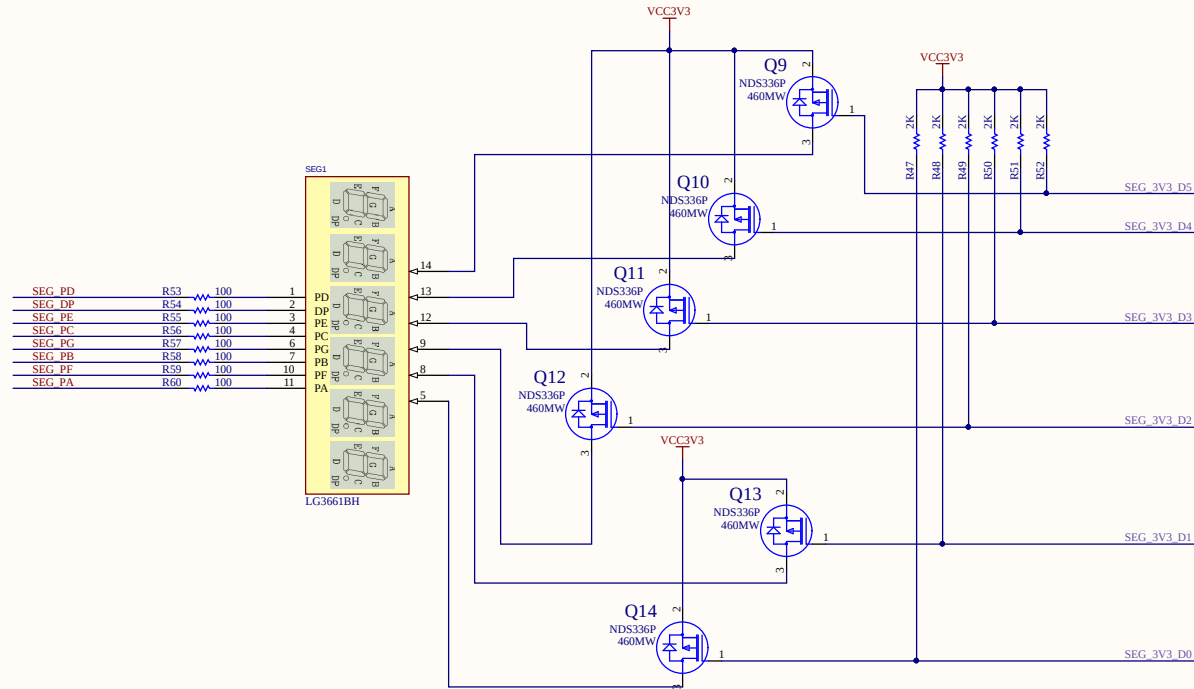


$$V_{out} = 0.6 \times (1 + R1/R2)$$

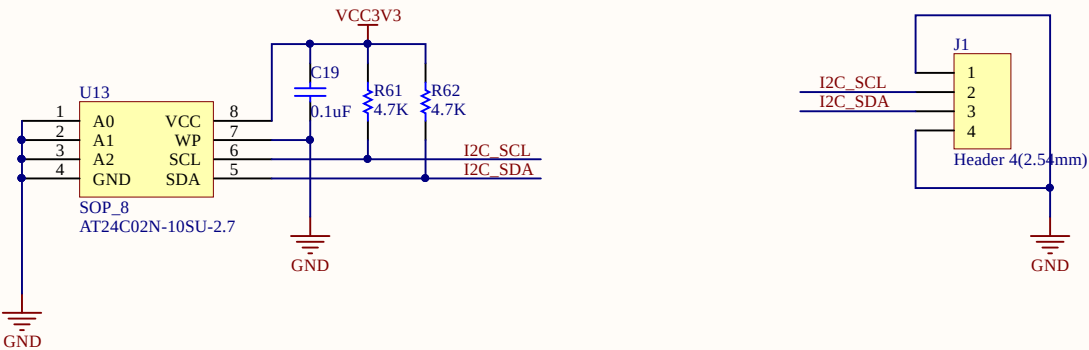
GPIO



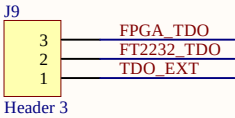
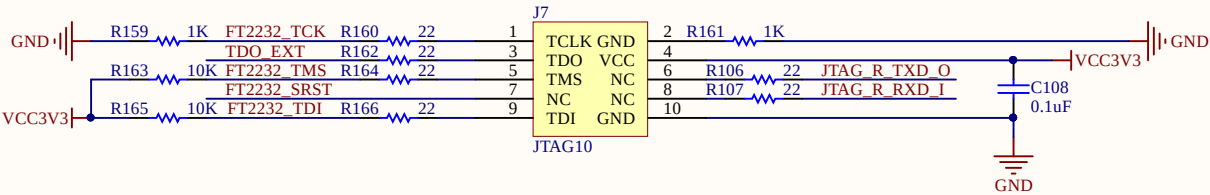
SEG



I2C

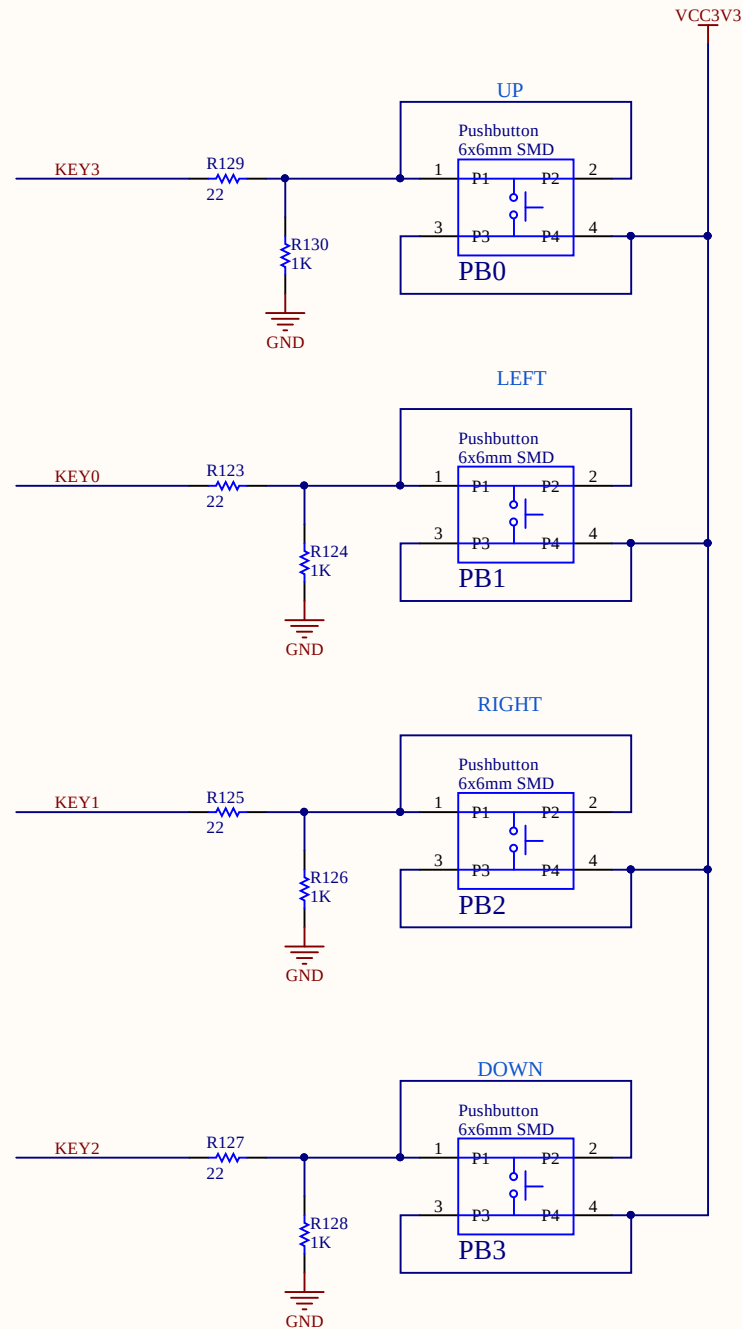


JTAG



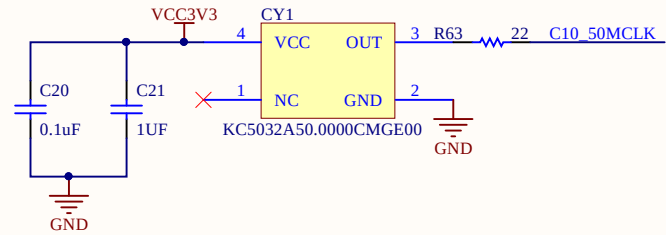
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Date: 2019-07-20	Time 3:41:33 PM	Engineer: WG RJ
Revision: R3.0	Sheet 4 of 14	Size: A4

KEYPAD

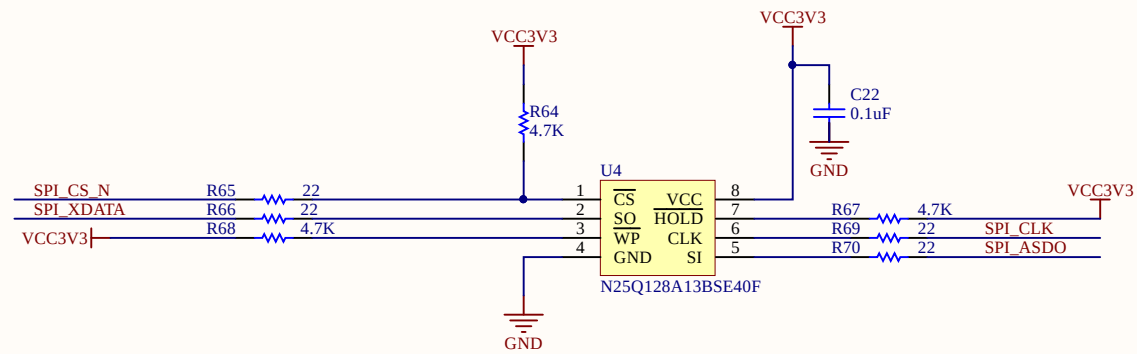


Company: FII	Title: BUTTON of PRA006		
Date: 2019-07-20	Time 3:41:33 PM	Engineer: WG RJ	Size: A4
Revision: R3.0	Sheet 5 of 14		

XTAL

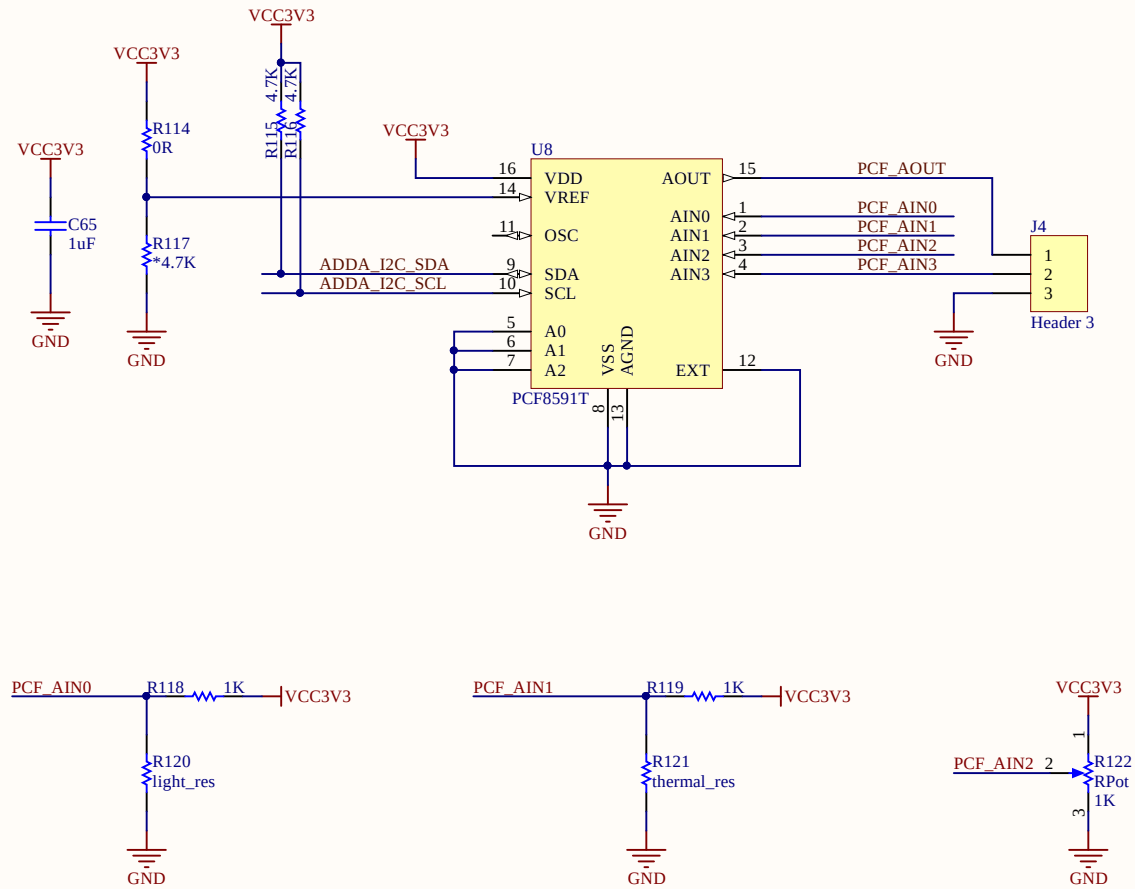


FLASH

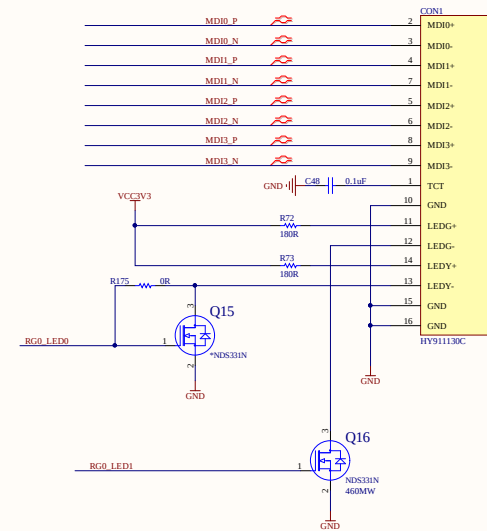
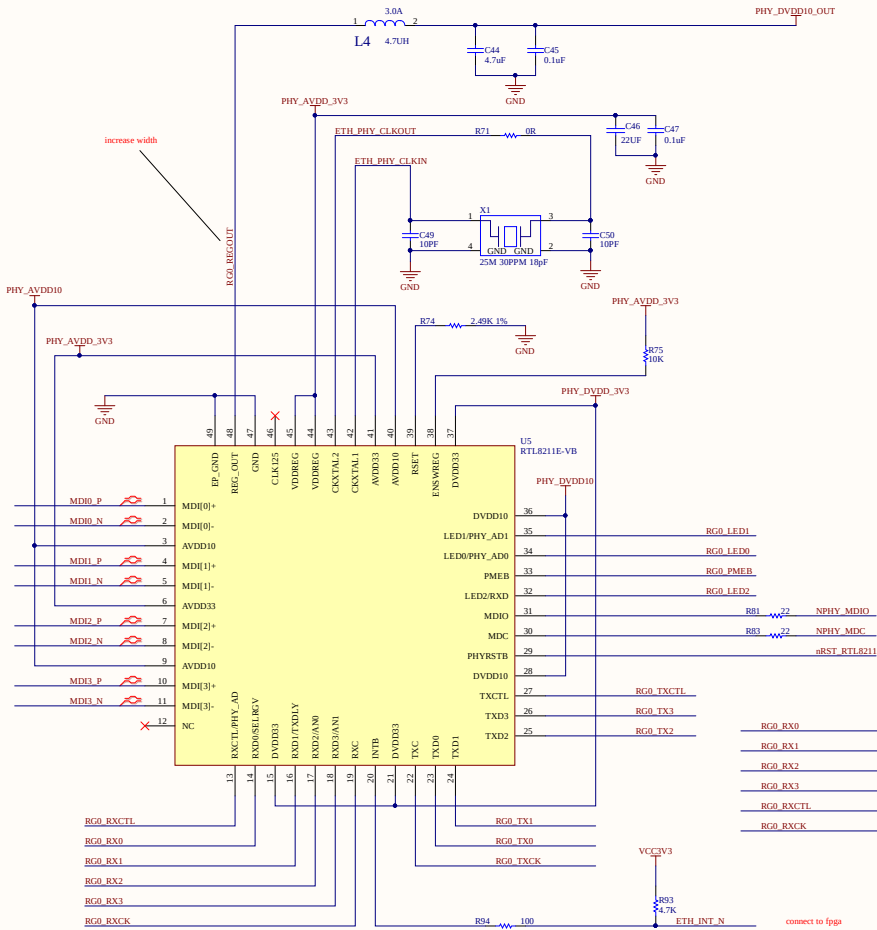
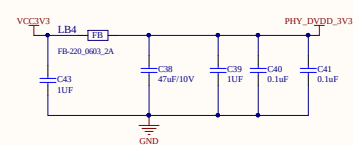
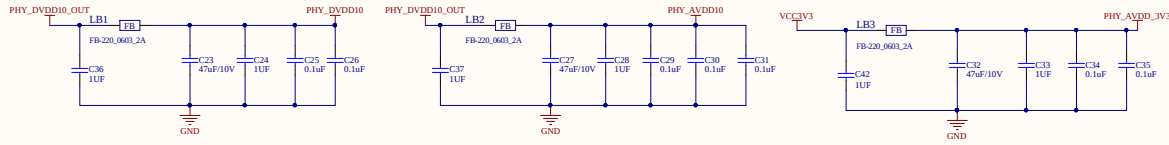


Company: FII		Title: OSC_FLASH of PRA006	
Date: 2019-07-20	Time 3:41:33 PM	Engineer: WG RJ	Size: A4
Revision: R3.0	Sheet 6 of 14		

AD/DA

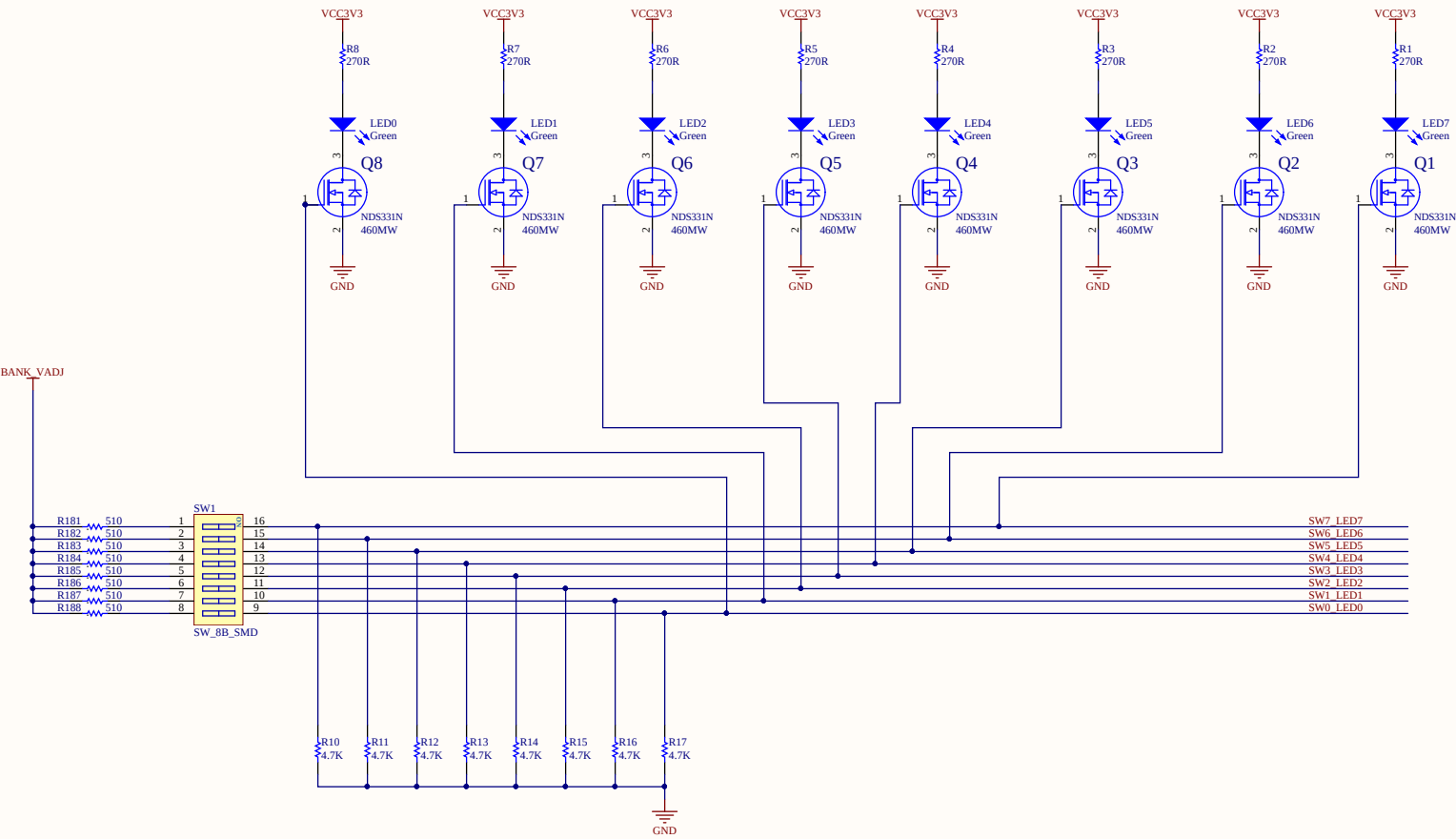


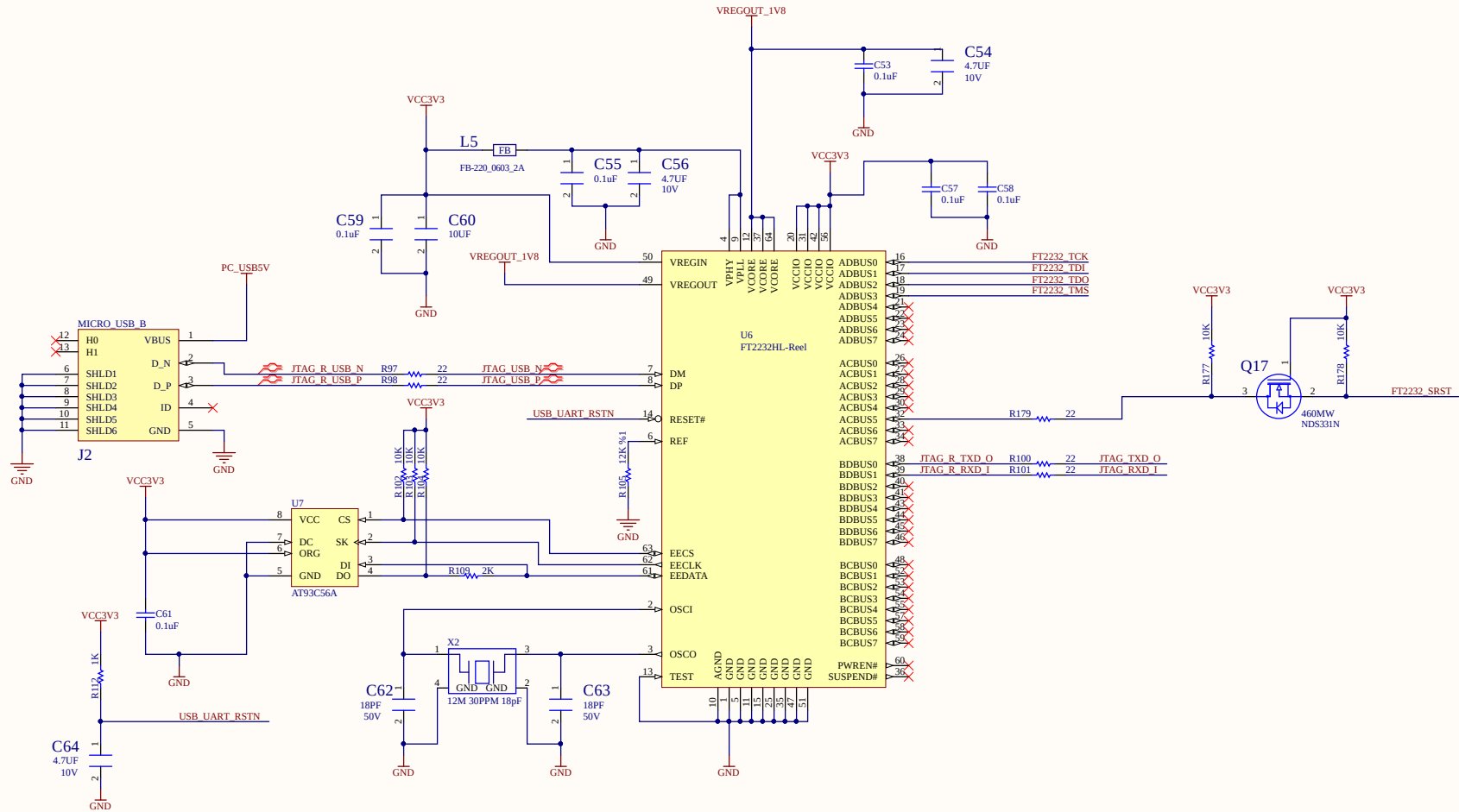
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Revision: R3.0	Sheet 7 of 14		

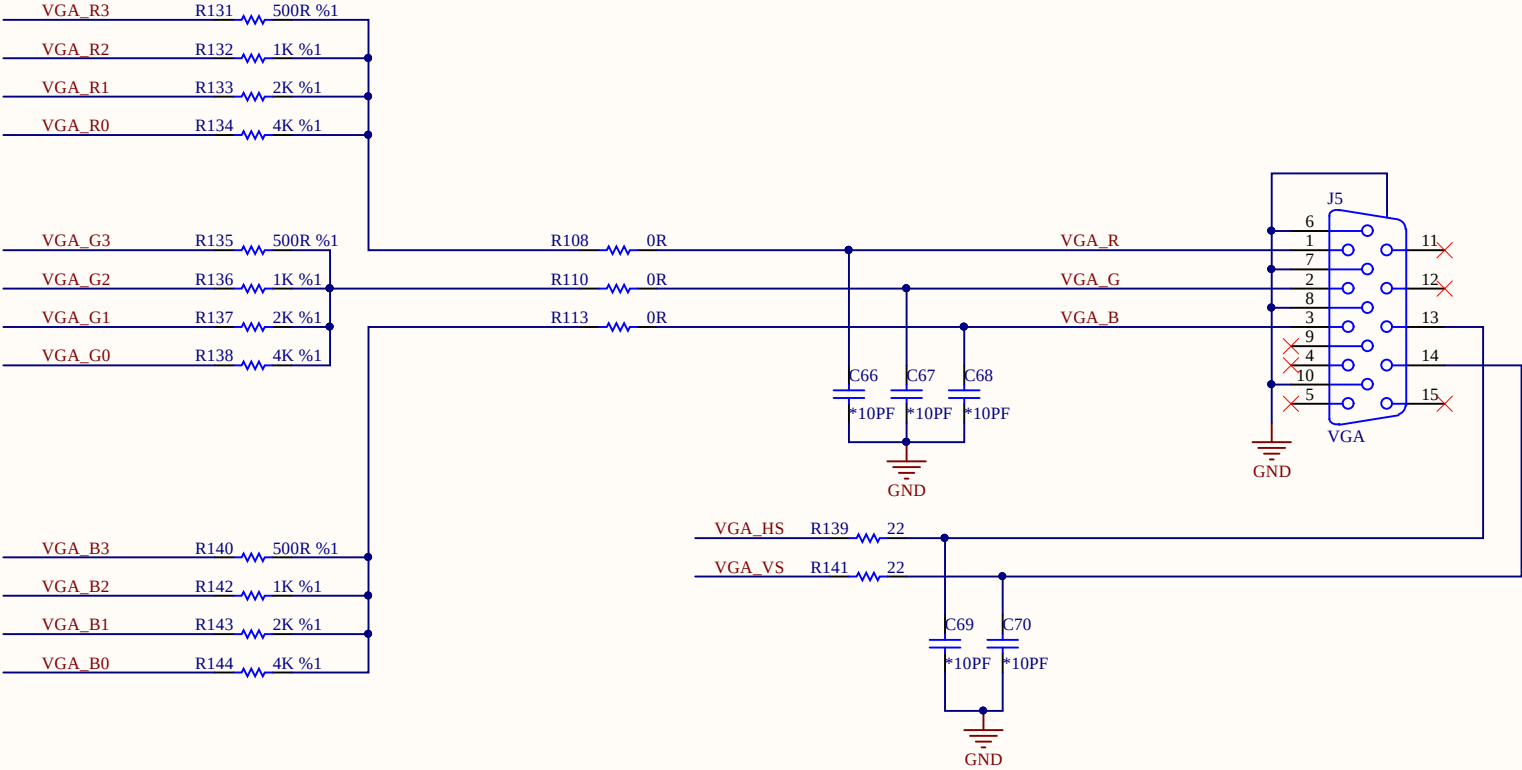


ETHERNET

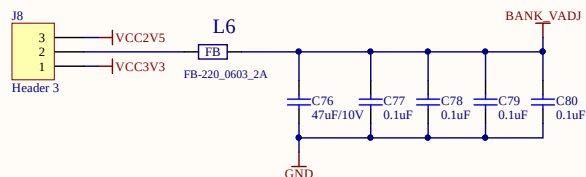
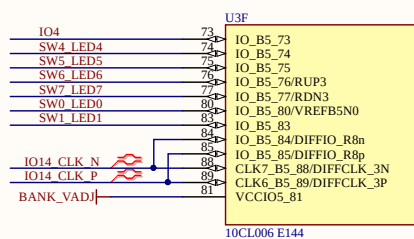
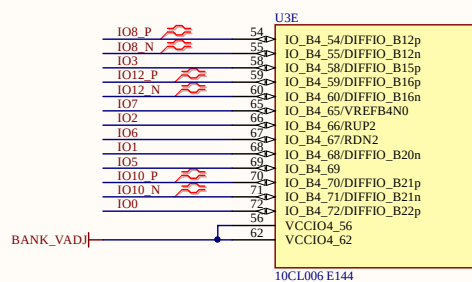
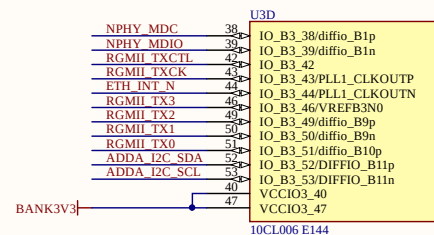
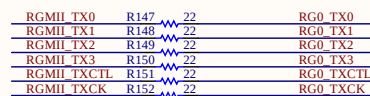
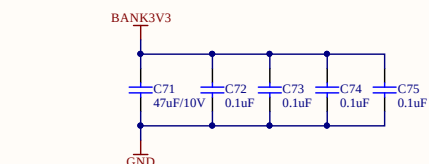
SWITCH
LED







Company: FII		Title: VGA of PRA006	
Date: 2019-07-20	Time 3:41:34 PM	Engineer: WG RJ	Size: A4
Revision: R3.0	Sheet 11 of 14		



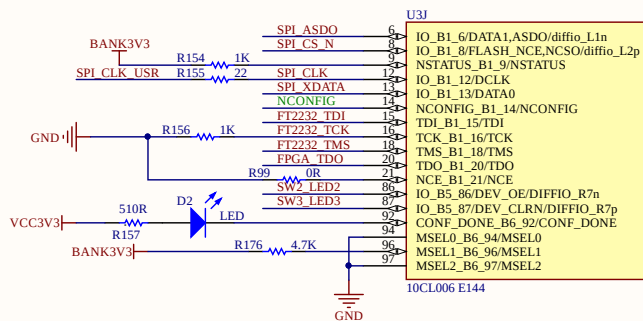


Table 46. MSEL Pin Settings for AS Configuration Scheme with MSEL[2:0] Pins

Configuration Scheme	Valid MSEL[2:0]	POR Delay	Configuration Voltage Standard (V)
AS	101	Fast	3.3
	010	Standard	3.3
	011	Standard	3.0
PS	100	Fast	3.3/3.0/2.5
	000	Standard	3.3/3.0/2.5
FPP	110	Fast	3.3/3.0/2.5
	111	Fast	1.8/1.5

